

Cadence Conformal Lec User Guide

Formal methods employ a variety of theoretical computer science fundamentals, including logic calculi, formal languages, automata theory, control theory, program semantics, type systems, and type theory.
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Formal methods Yorav, "Functional verification In computer science, formal methods are mathematically rigorous techniques for the specification, development, analysis,

and verification of software and hardware systems. Yarom, "Best known methods for using Cadence Conformal LEC", at Intel. C. 215-232. Seligman, I. Eisner, A. E. The use of formal methods for software and hardware design is motivated by the expectation that, as in other engineering disciplines, performing appropriate mathematical analysis can contribute to the reliability and robustness of a design. Nahir, K f334e6048a

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