

4 Bit Bidirectional Universal Shift Registers

ISO/IEC 2022 Originating in 1971, it was most recently revised in 1994. It is an 8-bit code using G0 and ISO/IEC 2022 Information technology—Character code structure and extension techniques, is an ISO/IEC standard in the field of character encoding. 41 and the Japanese Industrial Standard JIS X 0202. It is equivalent to the ECMA standard ECMA-35, the ANSI standard ANSI X3. CSI (in its 8-bit representation 0x9B), with the SDS (CSI ...]) CSI sequence being used for bidirectional text control

21:10, 14 Nov 2004 Mirv deleted Jim samuelson (consensus to delete, see [[Wikipedia:Votes for deletion/Jim... 21:17, 14 Nov 2004 Michael Snow deleted Village pump (news) (misplaced) The port, based on the inexpensive 9-pin D-connector, became a de facto standard through the 1980s and into the 1990s, supported by a wide variety of joysticks and other devices, most commonly paddle controllers, light pens and computer mice. The standard was so engrained that it led to devices like the Kempston Interface that allowed... In 1964, Texas Instruments introduced the SN5400 series of logic chips, in a ceramic semiconductor package. A low-cost plastic package SN7400 series was introduced in 1966 which quickly gained over 50% of the logic chip market, and eventually becoming de facto standardized electronic components. Since the introduction of the original bipolar-transistor TTL parts, pin-compatible parts were introduced with such features as low power CMOS technology and lower supply voltages. Surface mount packages exist for several popular logic family functions. 21:16, 14 Nov 2004 Adam Bishop deleted Andy warhol (content was: '{delete}')A good reference site all about Andy Warhol until someone else wants to update this page: [http://www.warhol.dk/ The Andy Warhol Homepage]...)

7400-series integrated circuits Retrieved 2023-07-21. Instruments. 2003. Fairchild The 7400 series is a popular logic family of transistor-transistor logic (TTL) integrated circuits (ICs). "74LCXZ16245 Low Voltage 16-Bit Bidirectional Transceiver with 5V Tolerant Inputs and Outputs" (PDF)

Atari joystick port It went cross-platform with the VIC-20 in 1981, and was

then used on many following machines from both companies, as well as a growing list of 3rd party machines like the MSX platform and various Sega consoles. setting one of four bits in the joystick shadow register, PORTA or PORTB, while the column set a bit on one of the trigger registers. The programmer then The Atari joystick port is a computer port used to connect various gaming controllers to game console and home computer systems in the 1970s to the 1990s. It was originally introduced on the Atari 2600 in 1977 and then used on the Atari 400 and 800 in 1979

Intel's original MCS-51 family was developed using N-type metal-oxide-semiconductor (NMOS) technology, like its predecessor Intel MCS-48, but later versions, identified by a letter C in their name (e.g., 80C51) use complementary metal-oxide-semiconductor (CMOS) technology and consume less power than their NMOS predecessors. This made them more suitable... 21:11, 14 Nov 2004 Infrogmation deleted C.L. "Butch" Otter (substub; content was: '{{delete}}Congressman (District 1, Idaho) first elected in 2000.') 21:12, 14 Nov 2004 Adam Bishop deleted In 1784 Spain (content before blanking was: '== xyz ==123456789')

Wikipedia:Historical archive/Logs/Deletion log/November 2004 (1) jpg]]æ') 21:44, 11 Nov 2004 Rossami deleted Travellers' Pocket Medical Guide (VfD decision 4-1 21:17, 14 Nov 2004 Infrogmation deleted Bipod (content was: '{{delete}}a bipod is a two leged stedying device used on guns, camras, binoculars ect. Bidirectional search (content was: 'juy[[Image:Example. ') 7ae38ec69c

Wikipedia:Reference desk/Archives/Science/February 8–14 2006 broadcast data (TV channels) and whilst they have been modified to allow bidirectional data at pretty high rates that bandwidth has to be shared between all

Intel MCS-51 The architect of the Intel MCS-51 instruction set was John H. Intel's original versions were popular in the 1980s and early 1990s, and enhanced binary compatible derivatives remain popular today. Wharton. It is a complex instruction set computer with separate memory spaces for program instructions and data. 8/11/16-bit operations; hence it is mainly an 8-bit microcontroller Boolean processor with 17 instructions, 1-bit accumulator, 32 registers (4 × 8-bit, bit-addressable) The Intel MCS-51 (commonly termed 8051) is a single-chip microcontroller (MCU) series developed by Intel in 1980 for use in embedded systems

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ISO 2022 specifies a general structure which character encodings can conform to, dedicating particular ranges of bytes (0x00–1F and 0x7F–9F) to be used for non-printing control codes for formatting and in-band instructions (such as line breaks or formatting instructions for text terminals), rather than graphical characters. It also specifies a syntax for escape sequences, multiple-byte sequences beginning with the ESC control code, which can likewise be used for in-band... 7ae38ec69c

List of 7400-series integrated circuits indicated an industrial temperature range; this prefix had been dropped from the TI literature by 1973. Most recent 7400-series parts are fabricated in CMOS or The following is a list of 7400-series digital logic integrated circuits. In the mid-1960s, the original 7400-series integrated circuits were introduced by Texas Instruments with the prefix "SN" to create the name SN74xx. Due to the popularity of these parts, other manufacturers released pin-to-pin compatible logic devices and kept the 7400 sequence number as an aid to identification of compatible parts. However, other manufacturers use different prefixes and suffixes on their part numbers 7ae38ec69c

Wikipedia:Reference desk/Archives/Science/January 2006 disputing secular height trends (although they are more complex and bidirectional than you suggest) but that they do not represent genetic changes in 7ae38ec69c

List of computing and IT abbreviations Extensible Exchange Protocol BER—Basic Encoding Rules BER—Bit Error Rate BFD—Bidirectional Forwarding Detection BFD—Binary File Descriptor BFS—Breadth-First This is a list of computing and IT acronyms, initialisms and abbreviations 7ae38ec69c

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