

Fundamentals Of Digital Circuits

By Anand Kumar

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Neuromorphic computing A neuromorphic computer/chip is any device that uses physical artificial neurons to do computations. Recent advances have even discovered ways to detect sound at different wavelengths through liquid solutions of chemical systems. An article published by AI researchers at Los Alamos National Laboratory states that, "neuromorphic computing, the next generation of AI, will be smaller, faster, and more efficient than the human brain. ; Ajayan, A. S2CID 7357110. ; Kumar, D. (November 1, 2015). In recent times, the term neuromorphic has been used to describe analog, digital, mixed-mode analog/digital VLSI, and software systems that

Fundamentals Of Digital Circuits By Anand Kumar

implement models of neural systems (for perception, motor control, or multisensory integration). S. ". James, A. "Threshold Logic Computing: Memristive-CMOS Circuits for Fast Fourier Transform Neuromorphic computing is an approach to computing that is inspired by the structure and function of the human brain. P 2de0ad8cdf

The inputs to an ALU are the data to be operated on, called operands, and a code indicating the operation to be performed (opcode); the ALU's output is the result of the performed operation. In many designs, the ALU also has status inputs or outputs, or both, which convey information about a previous operation or the current operation, respectively, between the ALU and external... 2de0ad8cdf DACs are commonly used in music players to convert digital data streams into analog audio signals. They are also used in televisions and mobile phones to convert digital video data into analog video signals. These two applications use DACs at opposite ends of the frequency/resolution trade-off. The audio DAC is a low-frequency, high-resolution type while the video DAC is a high-frequency low- to medium-resolution type. 2de0ad8cdf

Fundamentals Of Digital Circuits By Anand Kumar

Metal-semiconductor junction It is the oldest type of practical semiconductor device. (In contrast, a rectifying semiconductor-semiconductor junction, the most common semiconductor device today, is known as a p-n junction. One of those clamp circuits used a single germanium diode to clamp a silicon transistor in a circuit configuration In solid-state physics, a metal-semiconductor (M-S) junction is a type of electrical junction in which a metal comes in close contact with a semiconductor material. M-S junctions can either be rectifying or non-rectifying. The circuits are now known as Baker clamps.). The rectifying metal-semiconductor junction forms a Schottky barrier, making a device known as a Schottky diode, while the non-rectifying junction is called an ohmic contact

Negative resistance Ltd. pp. source to Z in the text. ISBN 978-8120325968 In electronics, negative resistance (NR) is a property of some electrical circuits and devices in which an increase in voltage across the device's terminals results in a decrease in electric current through it. The I-V curve is the same. 274, 283-289. PHI Learning Pvt. Kumar, Anand (2004). Pulse and Digital Circuits

Fundamentals Of Digital Circuits By Anand Kumar

This is in contrast to an ordinary resistor, in which an increase in applied voltage causes a proportional increase in current in accordance with Ohm's law, resulting in a positive resistance. Under certain conditions, negative resistance can increase the power of an electrical signal, amplifying it.

2de0ad8cdf A key aspect... 2de0ad8cdf

Arithmetic logic unit It is a fundamental building block of many types of computing circuits, including the central processing unit (CPU) of computers, FPUs, and In computing, an arithmetic logic unit (ALU) is a combinational digital circuit that performs arithmetic and bitwise operations on integer binary numbers. point numbers. This is in contrast to a floating-point unit (FPU), which operates on floating point numbers. It is a fundamental building block of many types of computing circuits, including the central processing unit (CPU) of computers, FPUs, and graphics processing units (GPUs)

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Metal-semiconductor junctions are crucial to the operation of all semiconductor devices. Usually, an ohmic contact is desired so that electrical

Fundamentals Of Digital Circuits By Anand Kumar

charge can be conducted easily between the active region of...

Moore's law Rather than a law of physics, it is an empirical relationship. It is an observation of experience-curve effects, a type of observation quantifying efficiency gains from learned experience in production. Moore's law is an observation and projection of a historical trend. Within his editorial, he speculated that by 1975 it would be possible to contain as many as Moore's law is the observation that the number of transistors in an integrated circuit (IC) doubles about every two years. entitled "Cramming more components onto integrated circuits"

Negative resistance is an uncommon property which occurs in a few nonlinear electronic components. In a nonlinear device, two types of resistance can be defined: 'static' or 'absolute resistance', the ratio of voltage to current

Digital-to-analog converter An analog-to-digital converter (ADC) performs the reverse function. ISBN 81-203-1745-9, ISBN 978-81-203-1745-1. A Anand

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Kumar, Fundamentals of Digital Circuits. Ndjountche Tertulien, "CMOS Analog Integrated Circuits: High-Speed In electronics, a digital-to-analog converter (DAC, D/A, D2A, or D-to-A) is a system that converts a digital signal into an analog signal

There are several DAC architectures; the suitability of a DAC for a particular application is determined by figures of merit including: resolution, maximum sampling frequency... or current-switch emitter-follower (CSEF) logic. As the current is steered between two legs of an emitter-coupled pair, ECL is sometimes called current-steering logic (CSL),

The observation is named after Gordon Moore, the co-founder of Fairchild Semiconductor and Intel and former CEO of the latter, who in 1965 noted that the number of components per integrated circuit had been doubling every year, and projected this rate of growth would continue for at least another decade. In 1975, looking forward to the next decade, he revised the forecast to doubling every two years, a compound...

Fundamentals Of Digital Circuits By Anand Kumar

Semiconductor device fabrication Lienig, Jens; Scheible, Juergen (March 19, 2020). Steps such as etching and photolithography can be used to manufacture other devices such as LCD and OLED displays.

ISBN 978-3-030-39284-0 – via Google Semiconductor device fabrication is the process used to manufacture semiconductor devices, typically integrated circuits (ICs) such as microprocessors, microcontrollers, and memories (such as RAM and flash memory). It is a multiple-step photolithographic and physico-chemical process (with steps such as thermal oxidation, thin-film deposition, ion-implantation, etching) during which electronic circuits are gradually created on a wafer, typically made of pure single-crystal semiconducting material. Springer Nature. Silicon is almost always used, but various compound semiconductors are used for specialized applications. Fundamentals of Layout Design for Electronic Circuits 2de0ad8cdf

current-mode logic (CML) 2de0ad8cdf

Emitter-coupled logic p. PHI Learning. Pulse and Digital Circuits. 472. ISBN 978-81-203-3356-7 In electronics, emitter-coupled logic (ECL) is a high-

Fundamentals Of Digital Circuits By Anand Kumar

speed integrated circuit bipolar transistor logic family. ECL uses a bipolar junction transistor (BJT) differential amplifier with single-ended input and limited emitter current to avoid the saturated (fully on) region of operation and the resulting slow turn-off behavior. Kumar, Anand (2008). ECL Emitter Coupled Logic"; (PDF). Fundamental Digital Electronics 2de0ad8cdf

Signal-flow graph Electronic circuits Characterizing sequential circuits of the Moore and Mealy type, obtaining A signal-flow graph or signal-flowgraph (SFG), invented by Claude Shannon, but often called a Mason graph after Samuel Jefferson Mason who coined the term, is a specialized flow graph, a directed graph in which nodes represent system variables, and branches (edges, arcs, or arrows) represent functional connections between pairs of nodes. This mathematical theory of digraphs exists, of course, quite apart from its applications. Thus, signal-flow graph theory builds on that of directed graphs (also called digraphs), which includes as well that of oriented graphs. example, Dr Arthur Guyton's model of the cardiovascular system 2de0ad8cdf

Fundamentals Of Digital Circuits By Anand Kumar

Anant Agarwal MIT directory "Board of Directors | Tiler Corporation" Anant Agarwal is an Indian computer architecture researcher. He is the founder and CTO of Tiler, a fabless semiconductor company focusing on scalable multicore embedded processor design. He is the CEO of edX, a joint partnership between MIT and Harvard University that offers free online learning. Lang (2005). Foundations of Analog and Digital Electronic Circuits. He is a professor of electrical engineering and computer science at the Massachusetts Institute of Technology (MIT), where he led the development of Alewife, an early cache coherent multiprocessor, and has been director of the MIT Computer Science and Artificial Intelligence Laboratory. Morgan Kaufmann. ISBN 1-55860-735-8 2de0ad8cdf

The fabrication process is performed in highly specialized semiconductor fabrication... 2de0ad8cdf In ECL, the transistors are never in saturation, the input and output voltages have a small swing (0.8 V), the input impedance is high and the output impedance is low. As a result, the transistors change states quickly, gate delays are low, and the fanout capability is high... 2de0ad8cdf SFGs are most commonly used to represent signal flow in a

Fundamentals Of Digital Circuits By Anand Kumar

physical system and its controller(s), forming a cyber-physical system. Among their other uses are the representation of signal flow in various electronic networks and amplifiers... 2de0ad8cdf

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Fundamentals Of Digital Circuits By Anand Kumar

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